



1/6

FIG.1

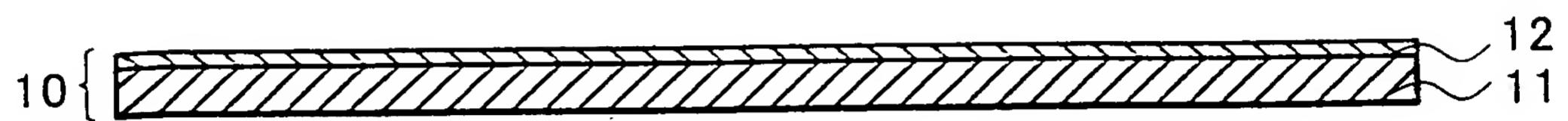


FIG.2

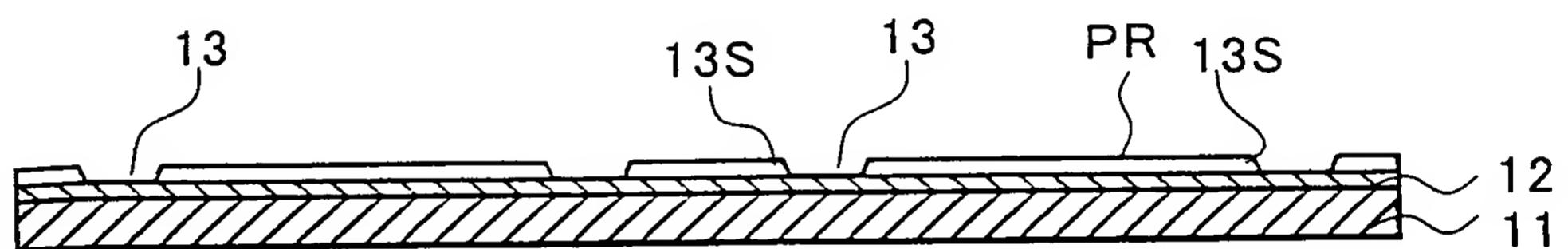


FIG.3

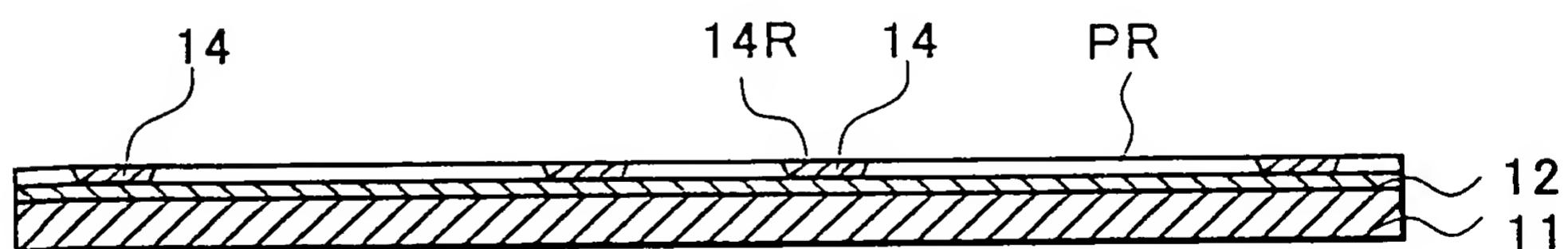




FIG.4

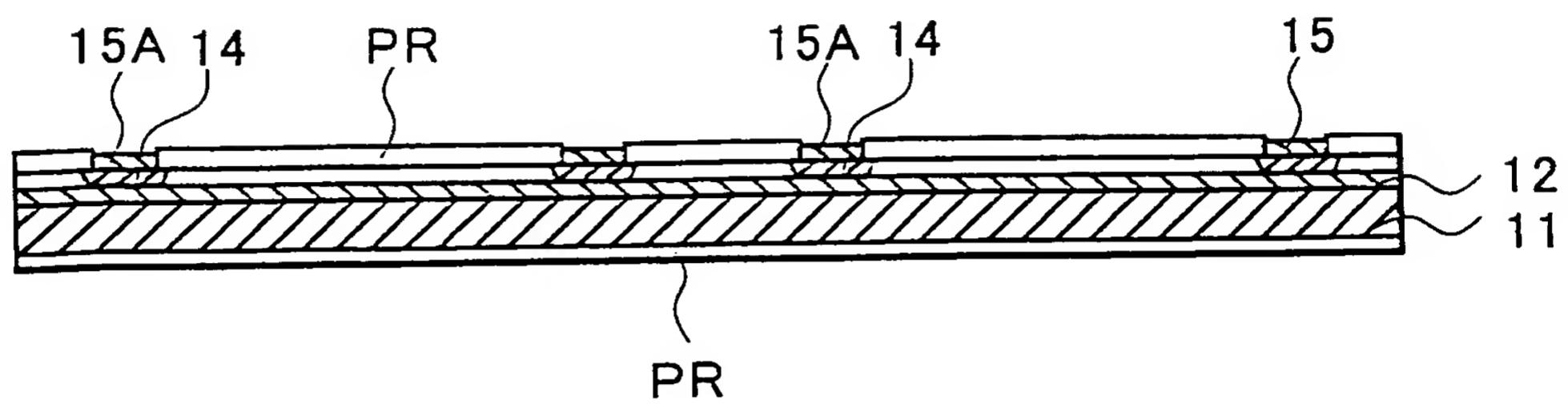


FIG.5

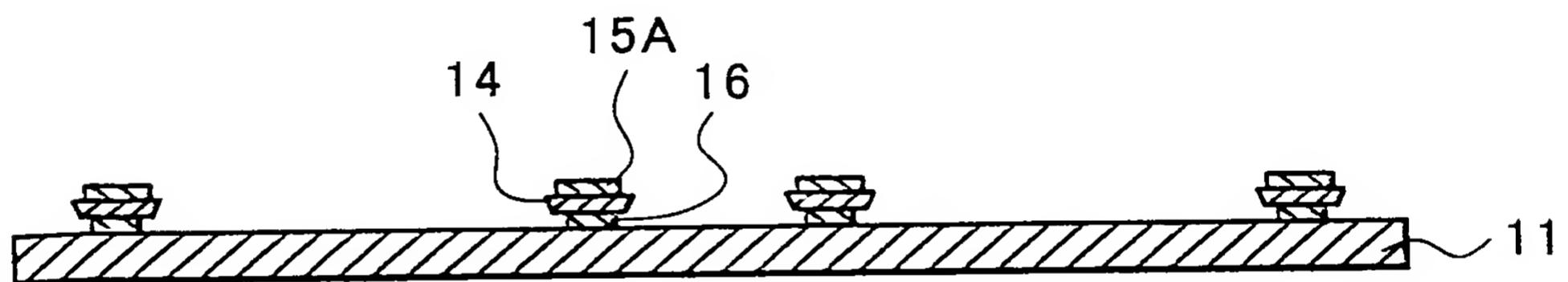


FIG.6

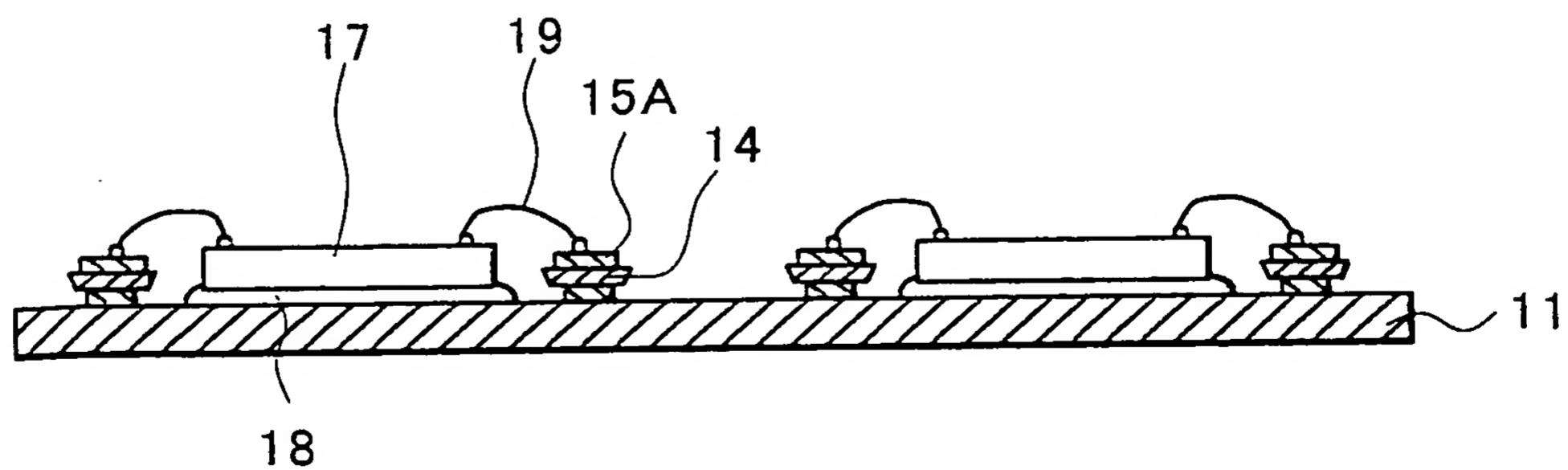




FIG.7

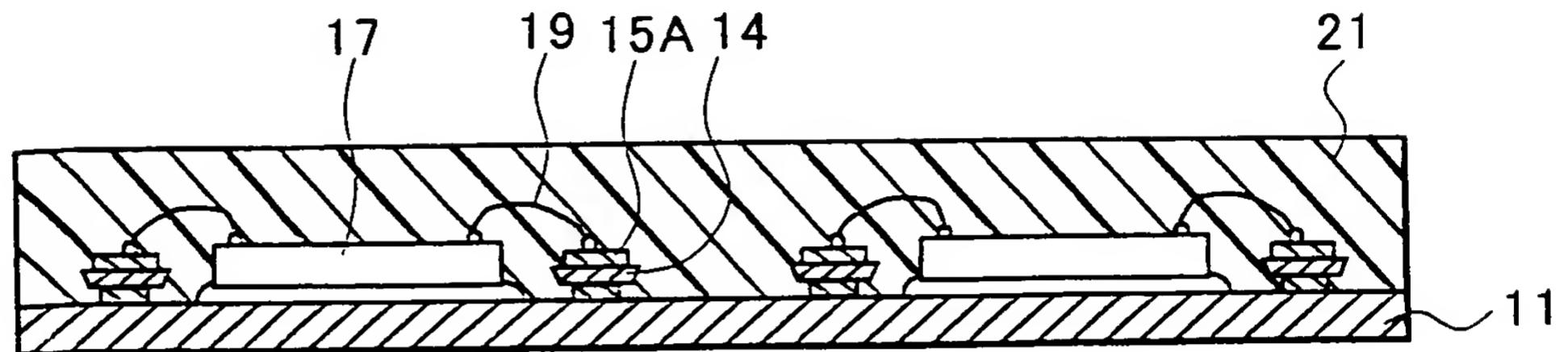


FIG.8

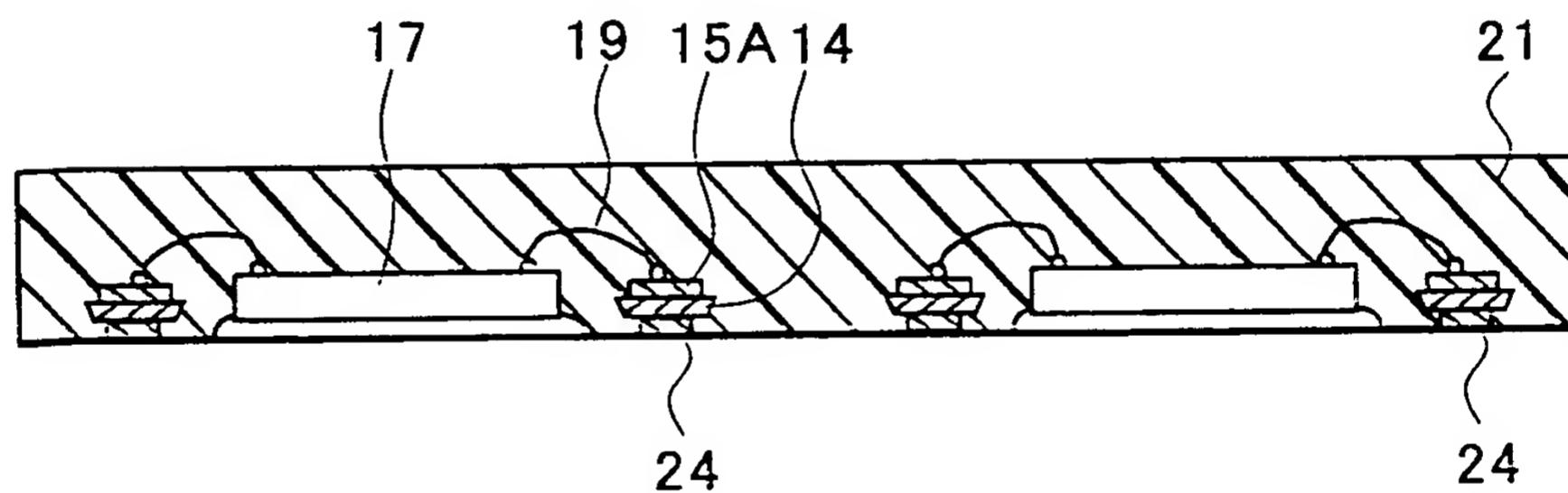
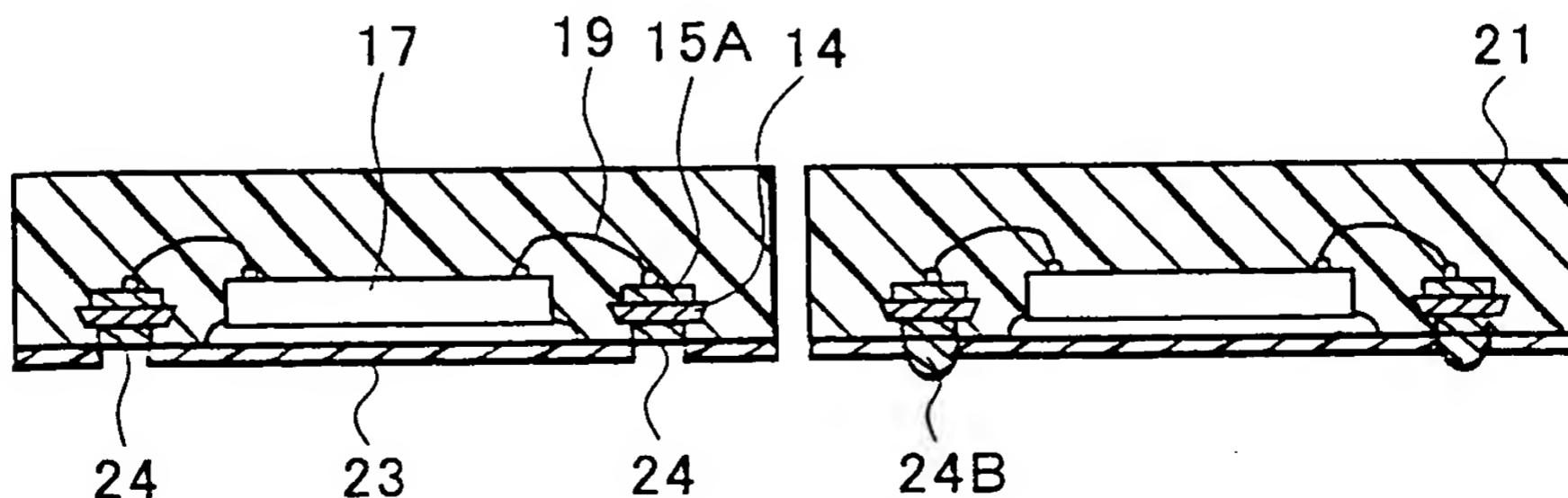


FIG.9





4/6

FIG. 10

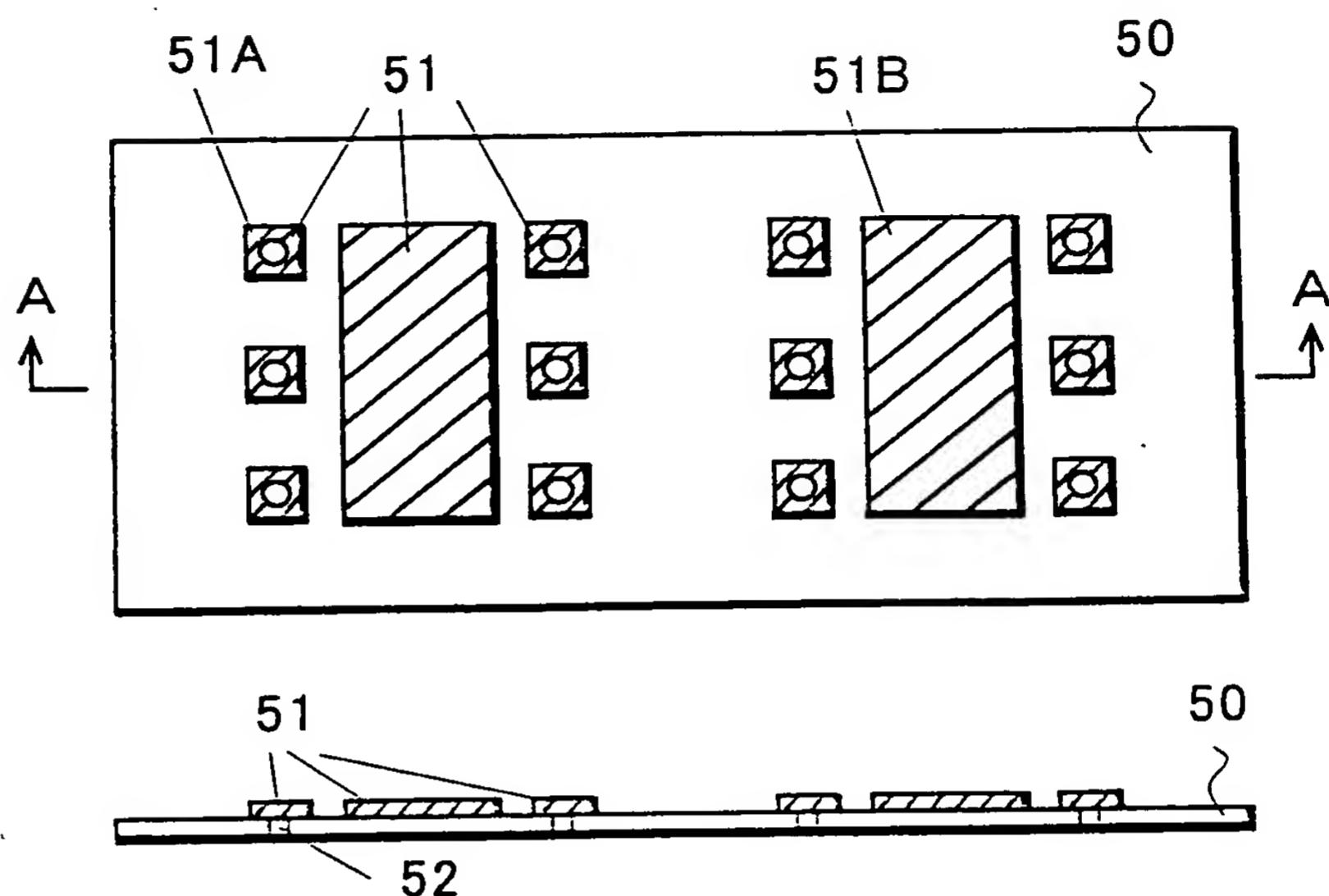
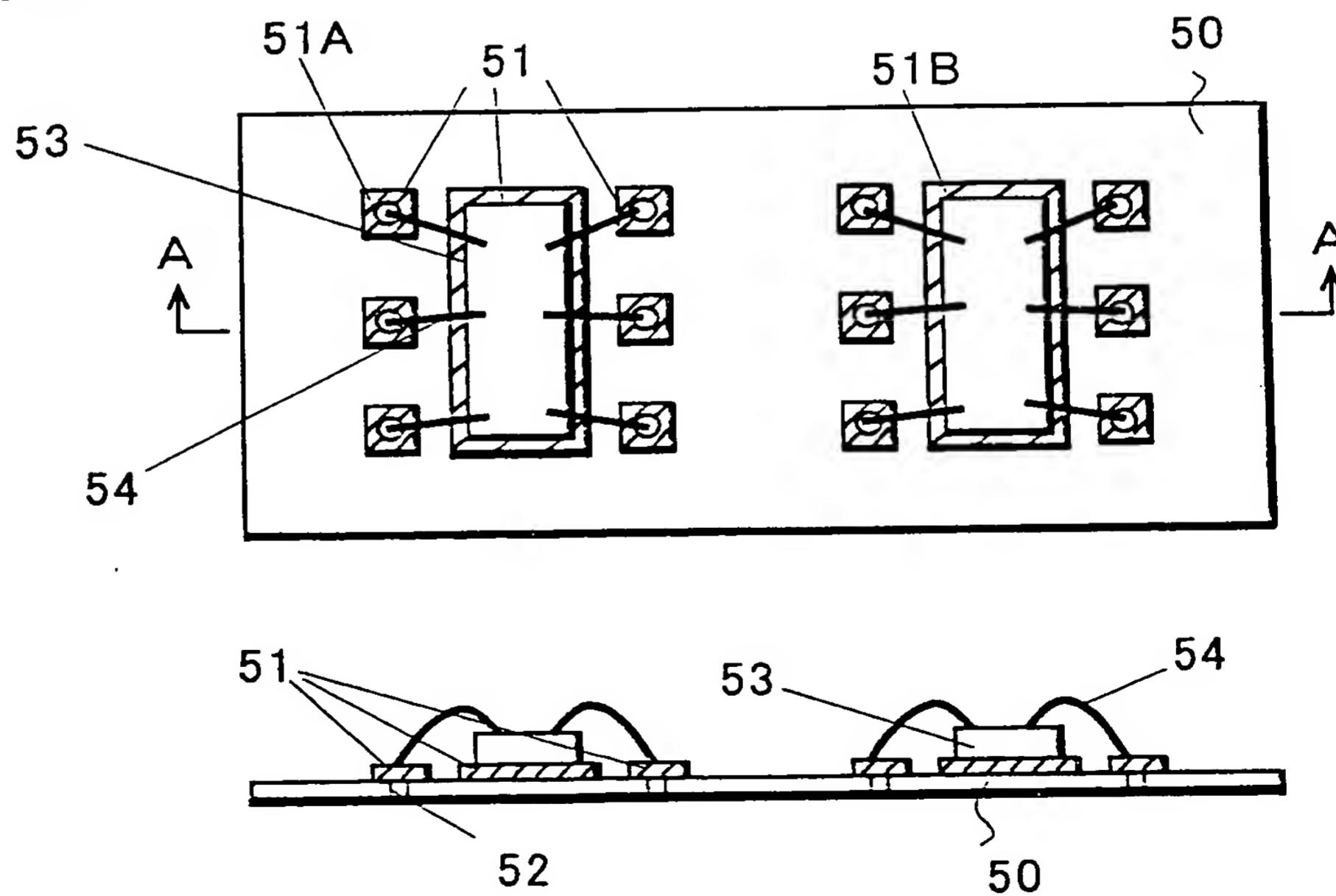


FIG. 11





5/6

FIG. 12A

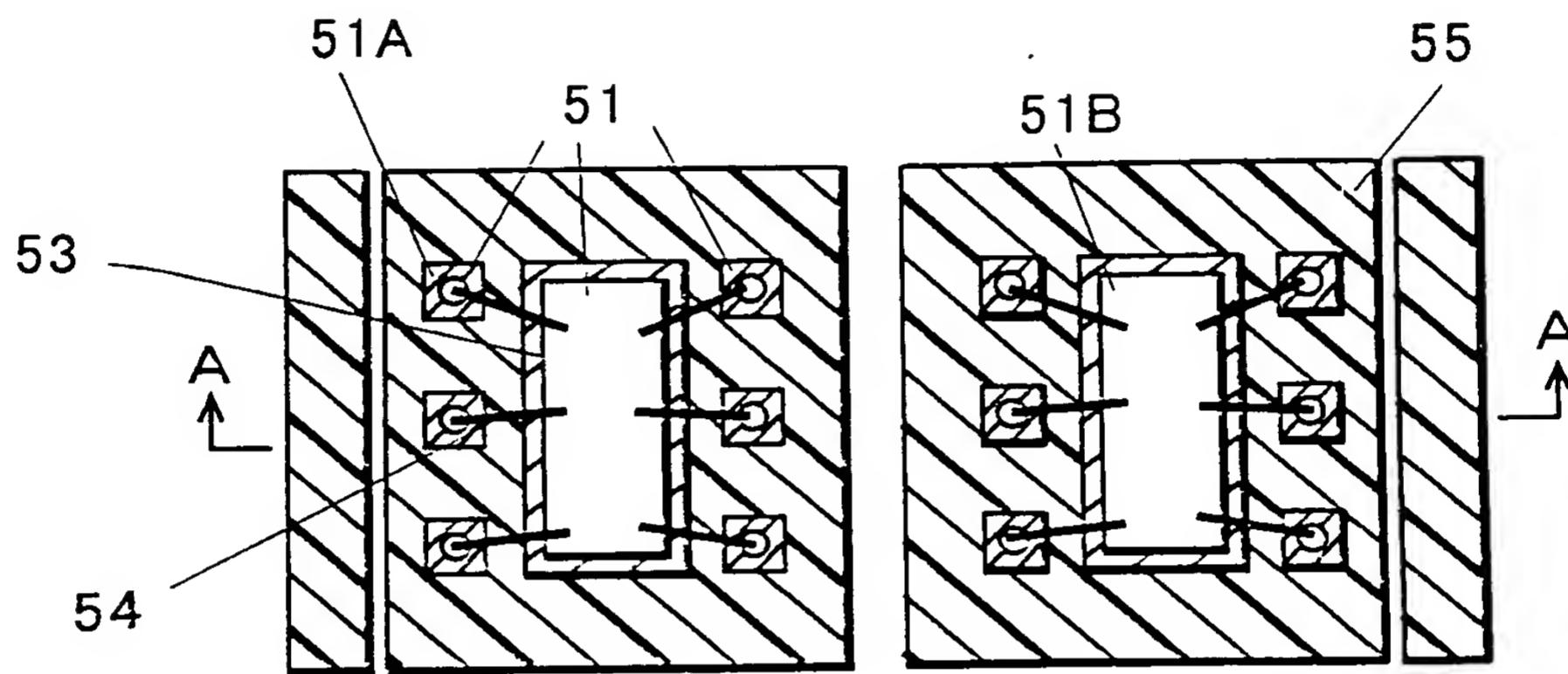


FIG. 12B

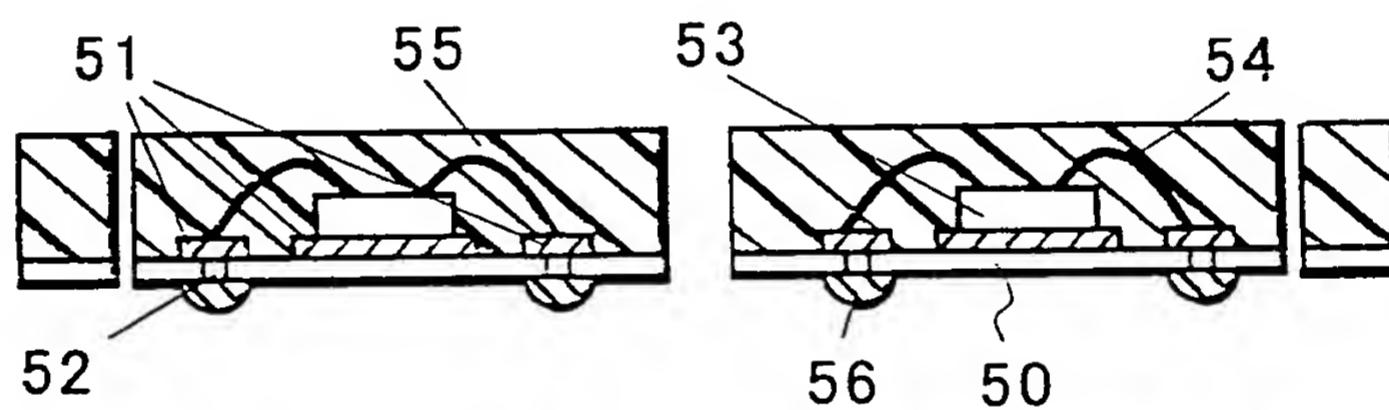
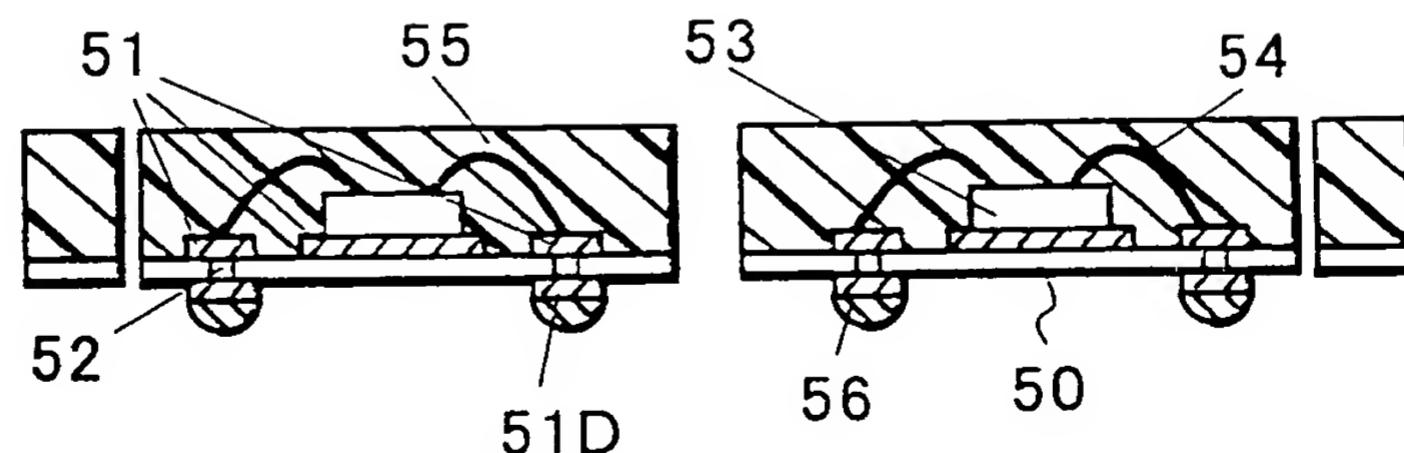


FIG. 12C





Matt. No.: 14225-027001  
Applicant(s): Yusuke Igarashi et al.  
METHOD FOR MANUFACTURING CIRCUIT DEVICES

Page of 6

6/6

FIG. 13

